


PTO-1449 INFORMATION DISCLOSURE STATEMENT BY APPLICANT 	ATTY. DOCKET NO. 2207/11986	Serial No. 09/944,222
	APPLICANT - Yongping FAN et al.	
	FILING DATE - September 4, 2001	Examiner - 2813 To Be Assigned <i>Dolan</i>

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS/SUBCLASS

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION	
					YES	NO

OTHER DOCUMENTS

EXAMINER INITIAL	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
<i>JMG</i>	Jed Griffin and David Johnson, "Large Signal Active Resistor Output Driver", IEEE 42 nd Symposium on Circuits and Systems (August 8-11, 1999).
<i>JMG</i>	Masashi Horiguchi et al., "A Tunable CMOS-DRAM Voltage Limiter with Stabilized Feedback Amplifier," IEEE Journal of Solid-State Circuits, Vol. 25, No. 5, October 1990.

EXAMINER <i>[Signature]</i>	DATE CONSIDERED <i>3/25/05</i>
EXAMINER: Initial if citation is considered, whether or not citation is in conformance with M.P.E.P. 609; strike out citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	